MODEL 7324 High-Speed Ruggedized 16-Ch Strain/Bridge/IEPE Ethernet DAS



FEATURES

- 16-Channels Strain Gage, Bridge, Voltage Transducers, Dynamic Strain or IEPE
- **Ethernet control and data interface**
- Real time temperature compensated (Gain & Excitation) from -20°C to +50°C
- 0.05% or better accuracy over full gain & temperature range
- 45 kHz analog bandwidth, gain 1 to 1,000
- 24-Bit A/D programmable up to 100kS/s per channel
- FIR filter, 128 tap
- IRIG A, B & G time decoding and recording
- 1 µS synchronized sampling of multiple enclosures
- Ruggedized for tough environments
- 20 to 30 Volt DC power

Pacific's Ruggedized Model 7300 increases the bandwidth and sample rates of the popular 7200 ruggedized data systems. A network appliance, each module conditions, amplifies and digitizes the outputs of up to sixteen strain gage, bridge or voltage transducers. The rugged enclosure is designed for installation in wind tunnels, engine test stands and other facilities where it is desired to locate the DAS close to the test article, ultimately reducing installation and cable costs as well as improving signal quality and reliability.

Fully self-contained, the 7324 provides transducer excitation, bridge completion and balance. The transducer output is amplified, filtered to remove signal components that could produce alias errors and digitized to 24-bit resolution. The digitized transducer output is digitally processed providing multiple sample rates, a 128 tap FIR filter and 32-bit floating point output format in millivolts referred to input, millivolts referred to output or user defined engineering units.

The Model 7324 supports strain gage and bridge transducers with programmable voltage excitation, bridge completion for 120 and 350 Ohm strain gages, shunt calibration, and bridge balance. The 7324 supports dynamic strain and IEPE transducers with constant current excitation and AC coupled inputs. The amplified transducer signals are digitized, processed, and output at up to 100,000 samples per second, as selected for individual channels. A 128 tap FIR filter is available for additional low-pass filtering.



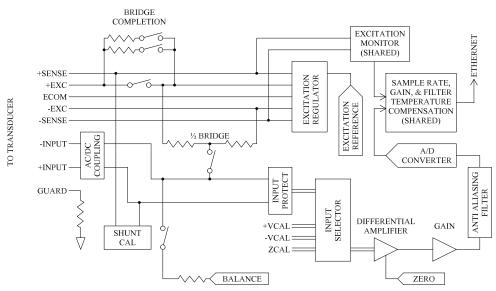
Each module has a 1 Gigabit Ethernet interface for control and data output, which simplifies system wiring and provides remote and distributed operation. IRIG time may be employed to synchronize sampling on multiple modules within 10 microseconds, or a TTL timing signal may be used to achieve better than 1 microsecond time alignment. Multiple modules with up to 4,096 channels may be connected on a single Ethernet LAN.

The module enclosure features interchangeable end plates that provide fully sealed, ambient air or forced air cooling. Forced air cooling provides the capability to operate over a wide temperature range according to application requirements. Occupying less than 500 cubic inches and weighing less than 14 pounds, the 7300 system can be installed in locations previously not suitable for data acquisition systems. The Ethernet interface and IRIG timing make it easy to distribute modules throughout the test facility, minimizing sensor cabling and installation costs.

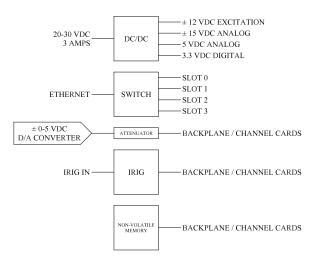
The 7300 does not sacrifice performance for size or portability; it features higher accuracy than most systems designed for a laboratory environment. Accuracy is better than 0.05% for gains up to 200 and 0.1% for gains above 200 over the temperature range -20° C to $+40^{\circ}$ C, which can be extended to higher temperatures by forced air cooling.

Model 7300 is supported by Pacific's PI770 software that provides a ready-to-run application and an SDK for user program development. Calibration and performance verification is accomplished by PANEL72 Maintenance and Calibration software. The ACS2000 Automatic Calibration System automates performance verification and periodic instrument calibration.





7324 Functional Block Diagram

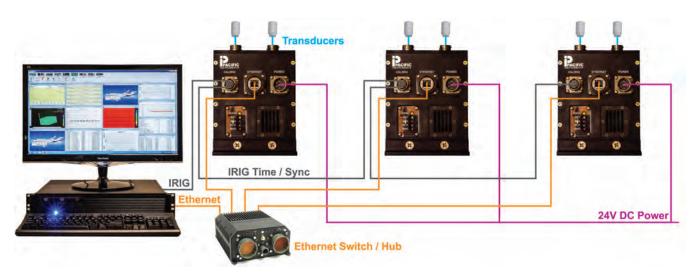


7X00 Controller Board Block Diagram

7300 INSTALLATION

Cabling transducers to a remote data acquisition system is costly and may require intermediate signal connections that introduce reliability problems. In addition, the transducer and cable impedance can filter the signal, attenuating high frequency signal components.

Locating data acquisition hardware near transducers at many test facilities requires specially constructed and environmentally conditioned enclosures. Using the 7300, the data acquisition system can be installed in unprepared locations and the only external connections are power, timing and Ethernet.



Typical 7300 Installation



ACCURACY

All 7300 models maintains $\pm 0.05\%$ uncertainty for gains up to 200 and $\pm 0.1\%$ for gains above 200 over the full temperature range by continuously correcting for temperature induced excitation, gain and zero deviations. The 7300 modules are calibrated and correction vectors are stored in non-volatile memory. An internal sensor determines the correction vectors applied to the data according to the device temperature. An internal programmable voltage reference can be applied to the channel inputs. The reference voltage can be externally monitored to verify accuracy.

SIGNAL CONDITIONING

The voltage excitation output is programmable from 0 to 15 Volts with better than 1 millivolt resolution. Calibrated 1 Volt steps are provided. Output current is 50 mA. Constant current excitation is available for dynamic strain or IEPE transducers. The constant current excitation has programmable steps of 2, 4, 5, 6, 7, 8, 9, 10, 15, 20, 25, 30, 35, 40, 45 and 50 mA. The excitation voltage is continuously monitored by sense inputs connected to the sensor and automatically adjusts as necessary to maintain a constant voltage over the full temperature range. Bridge completion is programmable for quarter-bridge, 120 and 350 Ohm gages, half and full bridges. Other values can be supplied on request. Two steps of shunt calibration are provided. Transducer balance employs a high-resolution DAC and is initiated by program instruction. AC coupling is available to support dynamic strain and IEPE transducers with current excitation.

INSTRUMENTATION AMPLIFIER

The sensor inputs are protected up to ± 50 Volts differential and ± 30 Volts common mode. They are applied to a high impedance, programmable gain, differential instrumentation amplifier. The amplifier has programmable gains from 1 to 1,000 providing full-scale input ranges of ± 10 mV to ± 10 Volts. Calibrated gain steps with $\pm 0.02\%$ accuracy are provided. Autozero maintains the amplifier output offset within ± 1 mV.

ANALOG TO DIGITAL

Conditioned and amplified transducer signals are digitized by a 24-bit resolution Sigma-Delta analog-to-digital converter that over-samples the analog signal enabling the use of a single frequency anti-aliasing filter.

SAMPLING

All channels are simultaneously sampled at the highest sample rate, 100 kS/s. Sampling is synchronized between channels and modules providing time aligned data using an external TTL timing signal or IRIG time. The TTL signal distributed to all modules provides 1 μ S time alignment for channels in the same or different modules. Sampling may be synchronized to IRIG A, B or G time. IRIG G time is generated by the unit connected to the external IRIG source or designated as the master time source if external IRIG is not used. Distributed to multiple modules it maintains channel to channel time alignment of 1 μ S for channels in the same module or 10 μ S for channels in other modules.

FILTER

Finite Impulse Response (FIR) filtering is provided. Eighty sets of 128 FIR filter coefficients are available in each 7300, providing an extensive selection of filter frequencies and characteristics. The coefficients are user programmable, and each coefficient is 18 bits in length. The filters are loaded with default coefficients providing logical filter cutoff selections and characteristics. The default filter frequencies and characteristics are easily changed using program instructions to satisfy future requirements.

DATA

Data are output in a digital format on the Ethernet interface. The output data is 32-bit floating point format and can be chosen via programming to be Volts or engineering units from a linear expression. Each system can output data from all channels at the maximum sample rate.

PROGRAMMING

All operating parameters can be set and queried using the extensive, built-in command set. Setup information is saved in password protected EEPROM memory on board, and it is automatically restored when power is applied or the system reset. The command set is well documented, and source code programming examples are provided.

SOFTWARE

PI770 is the choice for test management, data acquisition, and data display. Ready to run out of the box it is a 32-bit application that runs on Microsoft Windows operating system. It provides all the tools for test definition, setup and tracking, transducer calibration, data acquisition, display, replay and export, and test quality validation. PI770 comes with an Application Programmer's Interface that greatly reduces the development time for custom operating software. PANEL72 is Pacific's maintenance and calibration software. It may be used to troubleshoot installation problems and calibrate the 7300 modules.

COMMUNICATIONS

Initializing communication with the 7300 is simple. Commands are included that enumerate the 7300 devices on the network. After the enumeration operating software creates a socket interface between itself and each module, simple Send and Receive network commands are used to communicate. Status LEDs that assist with initial setup are located on the connector end of the enclosure.





SPECIFICATIONS

INPUT		
Configuration	Six-wire input, signal input (2), excitation (2),	
	excitation sense (2) and shield. Programmable	
	bridge completion for half bridges and 120 Ohm	
	and 350 Ohm quarter bridges. Programmable AC	
	or DC coupled input.	
Input Impedance	50 Megohms, shunted by 500 pF DC coupled,	
	100K Ohms AC coupled.	
Input Protection	±50 Volts differential, ±50 Volts common mode	
	without damage.	
Bridge Balance	Automatic by program control. Balance accuracy	
	$\pm 0.05\%$ of range, ± 1 mV RTO.	
AC Coupling	Programmable AC/DC coupling with 0.68 Hz -3dB	
	corner frequency for IEPE Transducers and	
	dynamic strain.	
VOLTAGE EXCITATION		
Voltage Excitation	Constant voltage with programmable local/remote	
	sensing and digital regulation, calibrated 1-Volt	
	steps from 1 to 15 VDC. 50 mA limited to 90	
Valtara English	mA maximum.	
voltage Excitation	1-Volt to 10-Volts ±3.75mV and 11-Volts to	
	15-Volts ±10mV over temperature range of -20 to +40°C.	
Evoltation Domilation		
Excitation Regulation	±0.01% over input voltage range and no-load to full load.	
Excitation Stability	±0.01%, 0.001%/°C	
-		
	200 μV peak-to-peak, DC to 10 kHz	
CURRENT EXCITA		
Current Excitation	Programmable 5 mA to 50 mA with calibrated	
	5 mA steps $\pm 0.1\%$.	
	24 Volts minimum.	
	$\pm 0.01\%$ or $\pm 0.1\mu A$ for 10% line change.	
Current Exc Noise	2 μA or 5 μV peak-to-peak DC to 10 kHz.	
Current Exc Stability	$\pm 0.01\%$ or $\pm 2~\mu A$ for 30 days. Temperature	
	coefficient is less than $\pm 0.005\%$ or $\pm 1 \ \mu\text{A/°C}$.	
AMPLIFIER		
Input Range	±10 mV to ±10 Volts, DC or AC coupled.	
Input Impedance	50 Megohms, shunted by 500 pF DC coupled,	
	100K Ohms AC coupled.	
Gain	Programmable from 1 to 1,000 with 0.02%	
	resolution. Thirteen calibrated gain steps are	
	provided: 1, 2, 3, 5, 10, 20, 30, 50, 100, 200,	
	300, 500, 1000 with ±0.05% accuracy up to	
Online Ohnel 1911	Gain 200, ±0.1% accuracy above Gain 200.	
Gain Stability	±0.01% for 30 days, ±0.001%/°C compensated.	
	±0.05% for 30 days, ±0.005%/°C without compensation.	
Gain Linearity	•	
	60 dB plus gain in dB to 110 dB, DC to 60Hz.	
	±10 Volts operating.	
Zel0	Automatic zero to $\pm 2 \mu V$ RTI or $\pm 1.0 \text{ mV}$ RTO whichever is greater.	
Zero Stability	$\pm 2 \ \mu V \ RTI \ plus \ \pm 1 \ mV \ RTO \ over the full$	
Zero Stability	temperature range.	
Source Current		
Bandwidth	±40 nA, ±0.4 nA/°C.	
	100 dB at 10 kHZ	
Overload	Recovery time is 120 μ S to within ±0.1% for a	
	10 times overload to ± 10 Volts.	
FILTER		
Туре	Finite Impulse Response (FIR) with 128	

DIGITIZED	
DIGITIZER	
	24-bits, Sigma-Delta type.
Rate	Individual channels may be programmed to output data at rates up to 100 kHz. Supports all sixteen channels at the highest sample rate.
TIME SYNCHRON	IZATION
Source	External TTL input or IRIG A, B or G. IRIG G output provided from each 7300 for multiple module synchronization.
Time Alignment	1 μ S using external TTL and 10 μ S using IRIG channel to channel and module to module.
Output	Days, Hours, Minutes, Seconds, Microseconds may be output at selected positions in the data stream.
ACCURACY	
Accuracy	The system maintains $\pm 0.05\%$ full scale (2- sigma) uncertainty for gains up to 200 and $\pm 0.1\%$ full scale (2-sigma) uncertainty for gains above 200 over the operating temperature range.
CALIBRATION	
Shunt	Two steps, single shunt. Internal or external
	shunt connection.
	174k and 357k Ohm, ±0.1%.
Voltage	The output of a temperature stable, 5-Volt internal reference with $\pm 0.01\%$ stability, X1.0, X0.1 and X0.01 $\pm 0.05\%$ attenuator and 0.0015% resolution on each attenuator setting may be applied to each channel. Accuracy is
	$\pm 0.2\%$ for gains to and including 100 and $\pm 0.3\%$ for gains over 100. Stability is $\pm 0.01\%$. Output of the internal reference can be measured on an external connector.
	Amplifier input disconnected and shorted for zero calibration.
INTERFACE / CON	
	Gigabit Ethernet, TCP/IP, control and data.
	Floating point, 32-bits. Output available in engineering units, Volts or millivolts.
	Supported by P1770 operational software and PANEL72 maintenance software.
MECHANICAL	
	MIL, 1/4-turn locking.
	Real time temperature compensated from -20°C to +40°C.
	MIL-STD-810F, Category 24.
	50 Gs, 11 mS all axis.
Moisture	NEMA 13. 20 to 30 VDC, 3A
	mounting flanges
Weight	
ORDERING INFOR	
	16-Ch Strain/Bridge/IEPE Data Acquisition (AC / DC coupled).
ALSO SEE	
	16-Ch Strain/Bridge Data Acquisition (DC coupled).
	16-Ch Voltage Data Acquisition (DC coupled).
7320	16-Ch Transducer Data Acquisition (DC coupled).

coefficients.